

# Towards grid-forming control for fault and overload ride through

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#### fuel

emissions & centralized

#### renewables

+ sustainable & decentralized







#### fuel & synchronous machines

- emissions & centralized
- + fully controllable generation

- + sustainable & decentralized
- intermittent & limited generation







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- + interoperable physics (& controls)

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- **slow** physics & actuation

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- heterogeneous & fragile controls
- low overcurrent & FRT poorly understood
- + fast actuation & flexible control

#### Grid-following vs. Grid-forming control

#### Grid-following control: renewables & maximum power point tracking



**Basic assumptions** 

- assumption: AC power system is an infinite AC bus
- converter model: AC current source feeding into an infinite AC bus

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- ► HVDC: stabilize DC voltage & track power reference

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Constraint handling through control of grid current

- converter limits: clip reference currents, momentary cessation, ...
- power source limits: control of power injection







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Constraint handling through control of grid current

- many heuristics for current limiting under faults
- ► few works on dc voltage, modulation, & power source constraints
- interaction of device-level protection and system-level protection?

#### Standard GFM control architecture for two-level VSCs



#### Cascaded dual-loop vector control & assumptions

- outer GFM control provides voltage reference  $\angle v^{\star} = \theta$ ,  $||v^{\star}|| = V$
- ▶ inner current and voltage control used to track GFM voltage reference
- ► DC bus **controlled** through DC source

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#### Functions of inner control loops

- **stiff** control of ac capacitor voltage **phase angle** & **magnitude**
- damping of filter resonance & enforcing timescale separation
- explicit current control & current limiting



#### GFM & standard current limiting

- ▶ set of voltages  $\mathcal{I}$  for which  $||i_f|| \leq I_{\max}$
- ► normal operation:
  - + GFM control points inside  ${\cal I}$
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  - loss of tracking & GFM synchronization



VSC

PCC

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#### Current limiting $\neq$ fault ride through

Transient stability resynchronization no overvoltage system protection

FRT

Harmonic stability current harmonics voltage harmonics Constraints: AC current DC voltage modulation

#### GFM control architectures revisited







- reuse GFL architecture
- ▶ industry standard (?)
- loss of synchronization

- well-studied in academia
- ► virt. impedance heuristics
- largely avoids loss of sync.

- very early stage
- no loss of sync.
- harmonic stability?

#### GFM control architecture with parallel limiter



Cascaded dual-loop vector control (optional)

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#### Threshold virtual impedance

- emulates increasing filter impedance as current approaches limit
- retains self-sync. voltage source dynamics behind variable impedance

#### GFM control architecture with outer limiter



Active damping for harmonic stability (optional)

▶ "modifies" circuit to compensate filter/grid resonance & harmonics

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#### Inner GFM oscillator

► GFM voltage reference  $\angle v^* = \theta$ ,  $||v^*|| = V$  always tracked by VSC

#### Outer loops: self-synchronization & constraints

- **•** synchronization of GFM oscillator states (e.g., P f droop)
- ► control of terminal voltage magnitude (e.g., *Q* − *V* droop)
- **constraint** handling functions (e.g., current, modulation, dc voltage)

#### Unbalanced fault ride through

- standard grid-forming control aims to impose balanced voltage
- does not allow full control of current in unbalanced system
- grid-forming control of symmetric components? phase voltages?



Relative Number of Different Types of Faults on HV Transmission Lines		
Fault Types	Percent	
Single Phase-to-Ground Faults	70	
Phase-to-Phase Faults	15	
Double Phase-to-Ground Faults	10	
Three Phase Faults	5	

### Reference current limiting (A-g fault)



#### Generalized GFM control leverages flexibility of VSCs

- ▶ pos./neg. sequence: intuitive for protection but VSC limits are per phase
- ▶ per-phase: straightforward handling of VSC limits
- per-phase can be *abc* or Clarke coordinates (e.g.,  $\alpha\beta$ )
- ▶ requires estimation of "phasors" for pos./neg. sequence or *abc*



#### (some) results from S-95

- unbalanced fault ride-through
- interaction with system-level protection

#### recent results on constrained GFM

- architecture with outer constraint handling
- preliminary results for the balanced case

S-95: Reliable fault ride-through and protection of converter-dominated power systems under unbalanced conditions



- estimation and grid-forming control for every phase (DC midpoint grounded)
- phase-balancing feedback between single-phase grid-forming controls
- dual-loop current and voltage control
- current reference limiting or threshold virtual impedance for every phase

#### Generalized three-phase grid-forming control

▶ single-phase droop for every phase  $p \in \{a, b, c\}$  & phase-balancing

$$\frac{\mathrm{d}}{\mathrm{d}t}\delta_p^{\mathrm{gfm}} = \omega_0 + m_P(P_p^\star - P_p) - \sum_{l \in \mathcal{P} \setminus p} k_P(\delta_p^{\mathrm{gfm}} - \delta_l^{\mathrm{gfm}})$$
$$\tau \frac{\mathrm{d}}{\mathrm{d}t} V_{\delta,p}^{\mathrm{gfm}} = -V_{\delta,p}^{\mathrm{gfm}} + m_Q(Q_p^\star - Q_p) - \sum_{l \in \mathcal{P} \setminus p} k_Q(V_{\delta,p}^{\mathrm{gfm}} - V_{\delta,l}^{\mathrm{gfm}})$$

▶ phase-balancing gains  $k_P \in \mathbb{R}_{\geq 0}$  and  $k_Q \in \mathbb{R}_{\geq 0}$  control voltage unbalance



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$$\begin{bmatrix} \overline{\omega}_{\delta} \\ \delta_{a-b} \\ \delta_{b-c} \end{bmatrix} = \begin{bmatrix} -m_p & 0 & 0 \\ 0 & -\frac{m_p}{3k_p} & 0 \\ 0 & 0 & -\frac{m_p}{3k_p} \end{bmatrix} \begin{bmatrix} \overline{P}_{\delta} \\ P_{a-b} \\ P_{b-c} \end{bmatrix}$$
$$\begin{bmatrix} \overline{V}_{\delta} \\ V_{b-b} \\ V_{b-c} \end{bmatrix} = \begin{bmatrix} -m_Q & 0 & 0 \\ 0 & -\frac{m_Q}{3k_Q+1} & 0 \\ 0 & 0 & -\frac{m_Q}{3k_Q+1} \end{bmatrix} \begin{bmatrix} \overline{Q}_{\delta} \\ Q_{a-b} \\ Q_{b-c} \end{bmatrix}$$

#### GFM control of symmetrical components

$$\Delta \omega^+ = m_p^+ (P^{+\star} - P^+) \tag{1}$$

$$\Delta \omega^- = m_p^- (P^{-\star} - P^-) \tag{2}$$

$$\Delta V^{+} = m_{q}^{+} (Q^{+\star} - Q^{+}) \tag{3}$$

$$\Delta V^{-} = m_q^{-} (Q^{-\star} - Q^{-}) \tag{4}$$

- intuitive from protection point of view (?)
- complex limiting due to nonlinear relationship with VSC phase current limits
- does not control neg. sequence current during fault or improve unbalanced FRT
- droop on neg. sequence current allows to control neg. sequence current few tangible benefits over phase control approach

#### Current saturation

- limits current reference (no tuning parameters)
- Sub-cycle phase current limiting
- integrator wind-up and loss of synchronization
- Threshold virtual impedance
  - emulates increasing output impedance as current increases
  - difficult to tune: R/X ratio, activation threshold, filter time constants
  - Heuristic for short-circuit faults that can't handle phase jumps (e.g., line opening)











Fig. 1: Transmission system with distance relays

- Low converter rating (1 MW)
- · Distance relay at the grid side trips normally
- Distance relay at the inverter side may not trip due to limited IBR fault current capability
- When the fault resistance is 5 Ω, Relay 1 trips after Relay 2 trips (i.e., the system becomes a single-ended network).

Table 1: Mho Relay Setting			
Protection Zone	Percentage of the transmission line	Operation Delay time	
Zone 1	20 %	16.7 ms	
Zone 2	100 %	100 ms	
Zone 3	200 %	400  ms	



Fig. 3: When  $R_f = 1 \Omega$ , the impedance computed by relays (before R\_2 trips)



#### Distribution System with the Inverse Time Relay



Fig. 2: ABC-g fault with inverse time relay.



- · original GFM current limit and protection setting
  - · Relay one operates (a)
  - · Relay two misoperates (b)
- · original GFM current limit and reduced relay threshold
  - reduced thresholds by 30%
  - Both relays operate correctly (c,d)
- · original GFM current limit and protection setting
  - GFM current limits increased by 40%
  - Both relays operate correctly (e,f)

#### Cold Start of an Unbalanced Distribution System



Fig. 2: Simulation results of the cold start



Fig. 1: Single-line diagram of BUS 13 distribution system including one GFM and one GFL

- Distributed cold start with GFM converters: no central control or coordination used
- Loads are connected or disconnected based on local voltage and frequency measurements and time delays
- inrush current during startup of induction machine is limited by current saturation algorithm (CSA)

Three-phase GFM fully leverages controllability of three-phase VSCs

- Standard controls and current limiting applied to each phase to control phase current / voltage
- Phase-balancing trades off voltage / power unbalance / sharing of unbalanced load

Benchmark systems to study interaction with system protection

- ► Transmission benchmark with distance relays
- Distribution benchmark with inverse time relays, UFLS/UVLS, induction machines, GFL, etc.
- ► Tuning guidelines for protective relays in converter-dominated systems

Improved FRT performance for short-circuit faults

- ► Tuning methods for virtual impedance
- ► Hybrid threshold virtual impedance

Distributed cold-start methods for unbalanced distribution feeders using GFM converters

Cold-start mechanisms that do not rely on centralized coordination

Towards constrained GFM control

#### Inner loops become outer loops



- GFM voltage oscillator never "cut off"
- ▶ synchronization slowly changes GFM oscillator phase angle
- voltage magnitude control at terminal
- ► fast control of GFM oscillator to satisfy constraints



dc-link dynamics

$$v_{\rm dc}(t+1) = v_{\rm dc}(t) + \frac{\tau}{C_{\rm dc}v_{\rm dc}^{\star}}(P_{\rm pv}(t) - P_{\rm sw}(t))$$

Quasi-steady-state circuit model

• PV: 
$$i_{\text{pv}}(t+1) = i_{\text{ph}} - I_0(e^{\frac{qv_{dc}(t+1)}{\alpha KT}-1}) - \frac{v_{dc}(t+1)}{R_{\text{sh}}}$$

• power injection: 
$$P_{SW}(t) = \frac{b_{SW}b_g}{b_{SW}+b_{eq}}(\theta_{SW}(t) - \theta_g(t)), Q_g = b_g(V_f(t) - V_g(t))$$

• filter voltage: 
$$V_f = \frac{b_{SW}V_{SW}(t) + b_gV_g}{b_f + b_{SW} + b_g}$$

• current: 
$$i_{sw}(t) = Z_{eq}(v_{sw}(t) - v_{g}(t))$$

#### Models, objectives & constraints for grid-connected PV



 $\min_{\theta_{\rm SW}(t),\,V_{\rm SW}(t)} (\omega_{\rm SW}(t) - \omega_0)^2 + m_{\rm dc} (v_{\rm dc}(t+1) - v_{\rm dc}^{\star})^2 + \frac{1}{\tau_v} (V_f(t) - V_f^{\star})^2 + \frac{m_q}{\tau_v} (Q_g(t) - Q_g^{\star}(t))$ 

s.t. (circuit model, PV model, dc dynamics)

$$\begin{split} v_{\text{mpp}} &\leq v_{\text{dc}}(t+1) \\ v_{\text{max}} &\geq v_{\text{dc}}(t+1) \\ V_{\text{SW}}(t) &\leq \frac{\gamma}{2} v_{\text{dc}}(t) \\ i_{\text{pv}}(t+1) &\geq 0 \\ \|i_{\text{SW}}(t)\| &\leq i_{\text{max}} \end{split}$$

#### Objectives and insights from PD-GFM

Power systems standards distinguish nominal and fault response

- ▶ nominal: well-understood GFM controls & small-signal dynamics
- constrained: track GFM as close as possible under constraints

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#### Lessons learned from PD-GFM

- integral of constraint violation crucial for robustness
- quasi-steady-state model insufficient during faults
- primal-dual gradient descent not converging fast enough



- VSC at bus 3 uses constrained GFM control
- ▶ VSC at bus 2 uses dual-port GFM control & energy storage
- various loads and perturbations at bus 3

#### Simulation results: high impedance fault



#### Simulation results: DC voltage limit



#### Simulation results: reactive load



#### Simulation results: reverse PV current



#### **Conceptual implementation**



#### High-level approach

- "outer" GFM control provides small-signal reference
- compute set of VSC voltages that do not violate constraints
  - one-step set: dynamic model of filter circuit
  - steady-state set: using quasi-steady-state model
- reset GFM control state to within constraints
  - restrict GFM control states to one-step set
  - · minimize integral of one-step and steady-state constraint violation

## 

#### Ideal response?

- ► execute GFM control
- compute integral of constraint violation
- compute nearest voltage in *I* that minimizes integral of constraint violation
- reset GFM control state





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